



Numerical Stability of Algorithms at Extreme Scale and Low Precisions

Nick Higham Department of Mathematics The University of Manchester https://nhigham.com

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The Limits of What We Can Compute

 $n \times n$ matrix prob.: rounding error bound f(n)u.

- ★ Problem dimension n
- ★ Unit roundoff u

both getting larger.

Increasingly **mixed precision world**: $u < u_1 < u_2 \cdots$.



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What can we guarantee about the computed solution?



TOP500: June 2022

- **Frontier** at Oak Ridge.
- AMD EPYC 64C 2GHz, AMD Radeon Instinct GPU. 8,730,112 cores
- Peak > 1.5 exaflops.
- IEEE double $u \approx 10^{-16}$, half $u \approx 10^{-3}$ or $u \approx 10^{-4}$.



Rate	
1.10 exaflops 6.86 " <mark>exaflops</mark> "	$\frac{2.4 \times 10^7}{2.7 \times 10^7}$

 $Petaflops = 10^{15} flops, Exaflops = 10^{18} flops$

Growth of Problem Size in TOP500

Dimension of matrix for #1 machine.

Machine	Date	n
Fugaku	2020	$2.0 imes10^7$
Jaguar	2010	$6.3 imes10^{6}$
ASCI RED	2000	$3.6 imes10^5$
CM-5/1024	1993	$5.2 imes10^4$

Growing by roughly a factor 10 every decade.



Today's Floating-Point Arithmetics

Bits					
Туре	Name	Signif. (t)	Exp.	Range	$u = 2^{-t}$
Quarter	fp8-e5m2	3	5	10 ^{±5}	1.2×10^{-1}
Quarter	fp8-e4m3	4	4	10 ^{±2}	$6.2 imes10^{-2}$
Half	bfloat16	8	8	10 ^{±38}	$3.9 imes10^{-3}$
Half	fp16	11	5	10 ^{±5}	$4.9 imes10^{-4}$
Single	fp32	24	8	10 ^{±38}	$6.0 imes10^{-8}$
Double	fp64	53	11	10 ^{±308}	$1.1 imes 10^{-16}$

Last three types are IEEE standard.

fp8 types introduced on NVIDIA H100 (2022).

Backward Error Analysis for LU Factorization

Let
$$\gamma_n = \frac{nu}{1-nu} = nu + O(u^2)$$
.

Theorem

Computed solution \hat{x} to Ax = b where $A \in \mathbb{R}^{n \times n}$ satisfies

$$(\mathbf{A} + \Delta \mathbf{A})\widehat{\mathbf{x}} = \mathbf{b}, \quad |\Delta \mathbf{A}| \leq \gamma_{3n}|\widehat{\mathbf{L}}||\widehat{\mathbf{U}}|.$$

Then for $n \approx 10^7$:

- in IEEE double precision, $nu \approx 2.3 \times 10^{-9}$.
- in IEEE single precision, $nu \approx 1.25$.

Sharper Bound

Proof uses $A + \Delta A_1 = \widehat{L}\widehat{U}$, where (recall $\gamma_n \approx nu$),

$$|\Delta A_{1}| \leq \begin{bmatrix} \gamma_{1} & \gamma_{1} & \cdots & \cdots & \gamma_{1} \\ \gamma_{1} & \gamma_{2} & \cdots & \ddots & \gamma_{2} \\ \vdots & \vdots & \ddots & \ddots & \vdots \\ \vdots & \vdots & \ddots & \gamma_{n-1} & \gamma_{n-1} \\ \gamma_{1} & \gamma_{2} & \cdots & \gamma_{n-1} & \gamma_{n} \end{bmatrix} \circ |\widehat{L}||\widehat{U}|. \quad (*)$$

Not fruitful to try to use (*).



Low Precision in Deep Learning

- "We find that very low precision is sufficient not just for running trained networks but also for training them."
 —Courbariaux, Benji & David (2015)
- "Deep learning models ... are very tolerant of reduced-precision computations."—Dean (2019).

$$|\operatorname{fl}(x^Ty) - x^Ty| \leq nu|x|^T|y|.$$

fp16: nu = 1 for n = 2048bfloat16: nu = 1 for n = 256

Yet deep learning successfully uses half precision.

The (Partial) Explanation

- Inner products not computed in the obvious way but are blocked ⇒ much smaller error bounds possible.
- We use **blocked algorithms**.
- Hardware features automatically boost accuracy.
- The rounding error bounds are worst-case and very pessimistic. Probabilistic error bounds are more insightful.

The (Partial) Explanation

- Inner products not computed in the obvious way but are blocked ⇒ much smaller error bounds possible.
- We use blocked algorithms.
- Hardware features automatically boost accuracy.
- The rounding error bounds are worst-case and very pessimistic. Probabilistic error bounds are more insightful.

Blocking is done for speed but also improves accuracy.



Blocked Inner Products: 2 Pieces

Original

$$s = \sum_{i=1}^{n} x_i y_i \Rightarrow |s - \widehat{s}| \le n u |x|^T |y|.$$



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$$s = \sum_{i=1}^{n} x_i y_i \Rightarrow |s - \widehat{s}| \le n u |x|^T |y|.$$

Blocked, 2 pieces
Let
$$n = 2b$$
.
 $s_1 = x(1:b)^T y(1:b)$
 $s_2 = x(b+1:n)^T y(b+1:n)$
 $s = s_1 + s_2$
 $|s - \widehat{s}| \le (\frac{n}{2} + 1)u|x|^T |y|$.

Blocked Inner Products; k Pieces

Original

$$s = \sum_{i=1}^{n} x_i y_i \Rightarrow |s - \widehat{s}| \le n u |x|^T |y|.$$

Blocked, *k* pieces Let n = kb. $s_i = x((i-1)b+1:ib)^T y((i-1)b+1:ib), i = 1:k$ $s = s_1 + s_2 + \dots + s_k$ $|s - \widehat{s}| \le (\frac{n}{k} + k - 1) u|x|^T |y|.$



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Optimal $k = \sqrt{n}$:

$$|\boldsymbol{s}-\widehat{\boldsymbol{s}}| \leq 2\sqrt{n}\boldsymbol{u}|\boldsymbol{x}|^{T}|\boldsymbol{y}|.$$

Block Summation

Recursive summation of x_1, \ldots, x_n :

- 1 *s* = 0
- 2 for $i = 1: n, s = s + x_i$, end

Standard block summation:

• sum blocks of size *b* by recursive summation: (b-1)n/b = n - n/b additions

2 sum n/b partial sums by recursive summation: n/b - 1 additions

Idea: use a **more accurate method** in step 2. E.g., recursive summation at *higher precision*, *compensated summation*.



FABsum

Blanchard, H & Mary (2020).

Input: *n*-vector *x*, block size *b*, algs FastSum, AccurateSum.

- 1: for i = 1: n/b do
- 2: Compute $s_i = \sum_{j=(i-1)b+1}^{ib} x_j$ with **FastSum**.
- 3: end for
- 4: Compute $s = \sum_{i=1}^{n/b} s_i$ with **AccurateSum**.
 - **FastSum** is doing n n/b additions.
 - **AccurateSum** is doing n/b 1 additions.

FABsum Error Bound

FastSum:
$$\widehat{s} = \sum_{i=1}^{n} x_i (1 + \mu_i^f), \quad |\mu_i^f| \le \epsilon_f(n),$$

AccurateSum: $\widehat{s} = \sum_{i=1}^{n} x_i (1 + \mu_i^a), \quad |\mu_i^a| \le \epsilon_a(n).$

Theorem

The computed \hat{s} from **FABsum** satisfies

$$\widehat{\boldsymbol{s}} = \sum_{i=1}^{n} \boldsymbol{x}_i (1 + \mu_i),$$

 $|\mu_i| \leq \epsilon(n,b) = \frac{\epsilon_f(b) + \epsilon_a(n/b)}{\epsilon_f(b) + \epsilon_a(n/b)} + \epsilon_f(b)\epsilon_a(n/b).$



Error Bound for Recursive/Compensated

Take **FastSum** = recursive summation, **AccurateSum** = compensated summation. Then

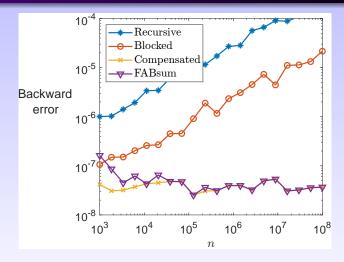
$$\epsilon(n,b) = (b+1)u + [4n/b+2+(b-1)^2+2(b-1)] u^2 + O(u^3).$$

Recall error bound is

- $nu + O(u^2)$ for recursive summation,
- $(n/b)u + O(u^2)$ for blocked summation.

FABsum error bound **independent of** *n* to first order.

Random Uniform [0, 1], *b* = 128, fp32



Blocked Matrix Multiplication

Let $A, B \in \mathbb{R}^{n \times n}$ be partitioned into $b \times b$ blocks A_{ij} and B_{ij} , where p = n/b is assumed to be an integer. This algorithm computes C = AB.

1 for
$$i = 1: p$$

2 for $j = 1: p$
3 $C_{ij} = 0$
4 for $k = 1: p$
5 $X = A_{ik}B_{kj}$
6 $C_{ij} = C_{ij} + X$
7 end
8 end
9 end

Compare
$$c_{rs} \leftarrow c_{rs} + a_{rk}b_{ks}$$

Blocked Algorithms

LAPACK philosophy: blocked matrix factorizations with a block size b = 128 or b = 256.

 \Rightarrow Reduction in error bounds by factor *b*.

At block level, apply block inner products giving further reduction!

- LAPACK manual states error bounds p(n)u, where "p(n) is a modestly growing function of n".
- Standard NLA refs don't mention *b* in error bounds.
 - Optimizing constants not the point (Wilkinson).
 - Constants depend on the block alg.
 - Analysis is more complicated.

Extended Precision Registers

- Intel x86-64 processors include 80-bit floating point registers with 64-bit significand (but not used by SSE2).
- Registers have $u = 2^{-64}$ rather than $u = 2^{-53}$ for double precision. Error bounds smaller by a factor up to $2^{11} = 2048$.
- Caveat: extra precision registers can lead to strange rounding effects, including double rounding!

Fused Multiply-Add (FMA)

Computes x + yz at same speed as "+" or "*" with just one rounding error.

Without an FMA,

$$\mathsf{fl}(x + yz) = (x + yz(1 + \delta_1))(1 + \delta_2), \quad |\delta_1|, |\delta_2| \leq u,$$

but with an FMA

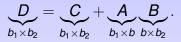
$$fl(x + yz) = (x + yz)(1 + \delta), \quad |\delta| \le u.$$

Error bounds for inner product-based computations reduced by a factor 2.

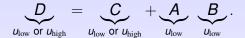
Mixed Precision Block FMA

Precisions u_{low} (fp8, bfloat16, fp16), u_{high} (fp16, fp32).

Dimensions:



Precisions:



Computation:

$$\mathsf{fl}_{\mathsf{high}}\Big(C + \mathsf{fl}_{\mathsf{high}}(AB)\Big).$$

Can chain: $C \leftarrow C + AB$.

Block FMA Hardware

Year	Device	Dimensions	<i>U</i> _{low}	U _{high}
2020	Google TPU v4i	$128 \times 128 \times 128$	bfloat16	fp32
2017	NVIDIA V100	$4 \times 4 \times 4$	fp16	fp32
2019	ARMv8.6-A	$2\times4\times2$	bfloat16	fp32
		$8 \times 8 \times 4$	bfloat16	fp32
2020	NVIDIA A100	$8 \times 8 \times 4$	fp16	fp32
		8 imes 4 imes 4	TFloat-32	fp32
		$2\times4\times2$	fp64	fp64

Note

- Not necessarily IEEE compliant.
- Very fast throughput ("one result per cycle") compared with none block-FMA arithmetic.

Error Analysis of Block FMAs

Blanchard, H, Lopez, Mary, & Pranesh (2020).

Analysis of algs for **matrix mult** C = AB based on block FMA. *Inherently multiprecision.*

For $A, B \in \mathbb{R}^{n \times n}$ using chained block $b \times b$ FMAs,

$$|\boldsymbol{C} - \widehat{\boldsymbol{C}}| \leq f(\boldsymbol{n}, \boldsymbol{b}, \boldsymbol{u}_{\mathrm{low}}, \boldsymbol{u}_{\mathrm{high}})|\boldsymbol{A}||\boldsymbol{B}|,$$

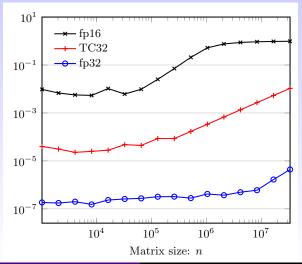
where with A, B given in $u_{high}, f(\cdot)$ is

Standard in precision $u_{\rm low}$	$(n+2)u_{\rm low}$
Block FMA, <i>u</i> _{high} internally	$2u_{\rm low} + nu_{\rm high}$
Standard in precision u_{high}	<i>nu</i> _{high}

Similar results for LU factorization and Ax = b.

NVIDIA V100

- Matrix entries are rand unif [0, 10⁻³].
- In fp32, cmp'wise error $\max_{i,j} |\widehat{C} C|_{ij}/(|A||B|)_{ij}$:



Probabilistic Error Analysis

Rounding error bounds above are worst-case.

"To be realistic, we must prune away the unlikely. What is left is necessarily a probabilistic statement."

- Stewart, 1990

Statistical Effects

"In general, the statistical distribution of the rounding errors will reduce considerably the function of *n* occurring in the relative errors. We might expect in each case that this function should be replaced by something which is no bigger than its square root."

— Wilkinson, 1961

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Limitations of central limit theorem argument

- Rounding errors independent random variables of mean zero.
- Applies only to **first-order** part of error.
- *n* is sufficiently large.

Standard Tool for Rounding Error Analysis

Theorem

If
$$|\delta_i| \leq u$$
 for $i = 1 : n$ and $nu < 1$ then

$$\prod_{i=1}^n (1+\delta_i) = 1+\theta_n,$$

where

$$|\theta_n| \leq \frac{\eta_n}{1-\eta_n} = \eta_n + O(u^2).$$

- The basis of most rounding error analyses.
- We seek an analogous result with a smaller, but probabilistic, bound on θ_n.



Assumptions for Probabilistic Analysis

Model M

Rounding error bound:

 $\mathsf{fl}(x \operatorname{\mathsf{op}} y) = (x \operatorname{\mathsf{op}} y)(1 + \delta), \quad |\delta| \le u, \quad \operatorname{\mathsf{op}} \in \{+, -, *, /\}.$

Mean independence:

The computation generates $\delta_1, \delta_2, \ldots$ that are random variables of mean zero such that

$$\mathbb{E}(\delta_{k+1} \mid \delta_1, \ldots, \delta_k) = \mathbb{E}(\delta_{k+1}) = \mathbf{0}.$$

• Weaker than assuming the δ_i are independent.

• The δ_i need not be from same distribution.

Probabilistic Analysis

Theorem (Connolly, H & Mary, 2021)

Let $\delta_1, \ldots, \delta_n$ satisfy Model M. For any constant $\lambda > 0$,

$$\prod_{i=1}^{n} (1+\delta_i) = 1 + \theta_n, \quad |\theta_n| \leq \frac{\widetilde{\gamma}_n(\lambda) \approx \lambda \sqrt{n} u}{\gamma_n(\lambda) \approx \lambda \sqrt{n} u},$$

holds with probability at least $1 - 2 \exp(-\lambda^2/2)$.

- Proof uses martingales.
- Valid for all n.
- Valid to all orders.
- Explicit probability $P(\lambda)$ (pessimistic).
- Earlier result by H & Mary (2020) assumes indep.

Inner Products

Theorem

Let $s = x^T y$, where $x, y \in \mathbb{R}^n$. Under Model M, the computed \hat{s} satisfies

$$\widehat{\mathbf{s}} = (\mathbf{x} + \Delta \mathbf{x})^T \mathbf{y}, \\ |\Delta \mathbf{x}| \le \widetilde{\gamma}_n(\lambda) |\mathbf{x}| \approx \lambda \sqrt{n} \mathbf{u} |\mathbf{x}|,$$

with probability at least $1 - 2n \exp(-\lambda^2/2)$.

Similar results by H & Mary (2020), Ipsen & Zhou (2020).



Linear Systems

Theorem

Under Model M, the computed solution \hat{x} to Ax = b from LU factorization satisfies

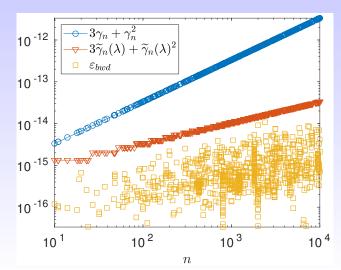
 $(\mathbf{A} + \Delta \mathbf{A})\widehat{\mathbf{x}} = \mathbf{b}, \quad |\Delta \mathbf{A}| \leq (\Im \widetilde{\gamma}_n(\lambda) + \widetilde{\gamma}_n(\lambda)^2)|\widehat{L}||\widehat{U}|,$

with probability at least $1 - 2n^3/3 \exp(-\lambda^2/2)$.



Real-Life Matrices

Solution of Ax = b (fp64), *b* from Uniform [0, 1], for 943 matrices from **SuiteSparse** collection ($\lambda = 1$).



Probabilistic QR Error Bound

Theorem (Connolly & H, 2022)

Under Model M and a technical assumption, for the computed $\widehat{R} \in \mathbb{R}^{m \times n}$ from Householder QR on $A \in \mathbb{R}^{m \times n}$ $(m \ge n), \exists$ orthogonal $Q \in \mathbb{R}^{m \times m}$ such that

$$\begin{aligned} \mathbf{A} + \Delta \mathbf{A} &= \mathbf{Q}\widehat{\mathbf{R}}, \\ \|\Delta \mathbf{a}_j\|_2 &\leq c\lambda\sqrt{mn}\,u\|\mathbf{a}_j\|_2 + O(u^2), \quad j = 1:\,n, \end{aligned}$$

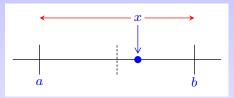
holds with probability at least $1 - 2mn \exp(-\lambda^2)$.

- Uses a matrix concentration inequality of Tropp (2012).
- Worst-case bound has mnu.
- Square rooting of constant applies to Givens QR, too.



Stochastic Rounding

Forsythe (1950), ..., Croci et al. (2022).



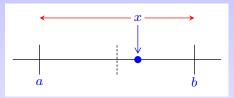
Theorem (Connolly, H & Mary, 2021)

The rounding errors $\delta_1, \delta_2, \ldots$ from stochastic rounding are rand. vars of mean 0 s.t. $\mathbb{E}(\delta_k | \delta_1, \ldots, \delta_{k-1}) = \mathbb{E}(\delta_k) = 0$.



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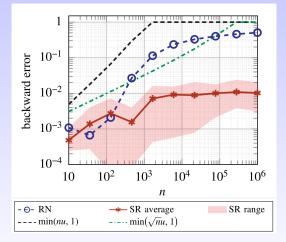
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Stochastic rounding always satisfies the assumptions!

For SR, we can *always* replace nu by \sqrt{nu} in a worst-case rounding error bound to obtain a probabilistic error bound.

Stagnation

Harmonic sum $\sum_{k=1}^{n} 1/k$ in fp16.



Stochastic rounding avoids stagnation!

Random Data

Model M'

d_j, *j* = 1 : *n*, are independent random variables from a distribution of mean μ_x s.t. |*d_j*| ≤ ξ_d, *j* = 1 : *n*.
 𝔼(δ_k | δ₁,...,δ_{k-1}, *d*₁,...,*d_n*) = 𝔼(δ_k) = 0.

Theorem (H & Mary, 2020)

If $A \in \mathbb{R}^{m \times n}$ and $B \in \mathbb{R}^{n \times p}$ Model M', with means μ_A , μ_B and bounds ξ_A , ξ_B , and let C = AB. Under Model M',

$$\max_{i,j} |(\boldsymbol{C} - \widehat{\boldsymbol{C}})_{ij}| \leq (\lambda |\mu_A \mu_B| n^{3/2} + (\lambda^2 + 1)\xi_A \xi_B n) u + O(u^2)$$

with probability at least $P(\lambda) = 1 - 2mnp \exp(-\lambda^2/2)$.



Putting It All Together

- Block algs reduce error bound by factor *b*.
- For blocking at multiple levels, the reduction factors can accumulate.
- Extended precision registers and (block) FMAs give automatic accuracy boost.
- Block size b = 256 and 80-bit registers reduces error bound by factor 256 × 2048 = 5.2 × 10⁵.
- Prob error anal. says " $f(n)u \rightarrow \sqrt{f(n)}u$ ".
- Prob. error anal. applies to blocked algs. Error constant (b+n/b)u for a blocked inner product translates to $(\sqrt{b} + \sqrt{n/b})u$ in a prob. bound.



Conclusions

- Classical analyses no longer guarantee the numerical stability of classical algorithms for all n and u of interest.
- Block algs (designed for speed) & hardware features give significant accuracy boosts.
- New probabilistic bounds show " $f(n)u \rightarrow \sqrt{f(n)}u$ ". Even these bounds often very pessimistic.
- We often do better than we can currently explain.

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